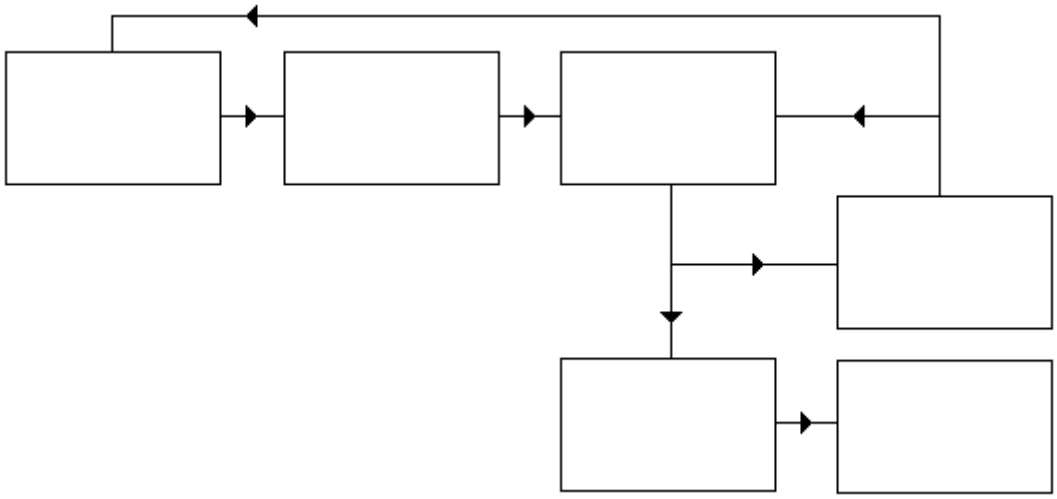
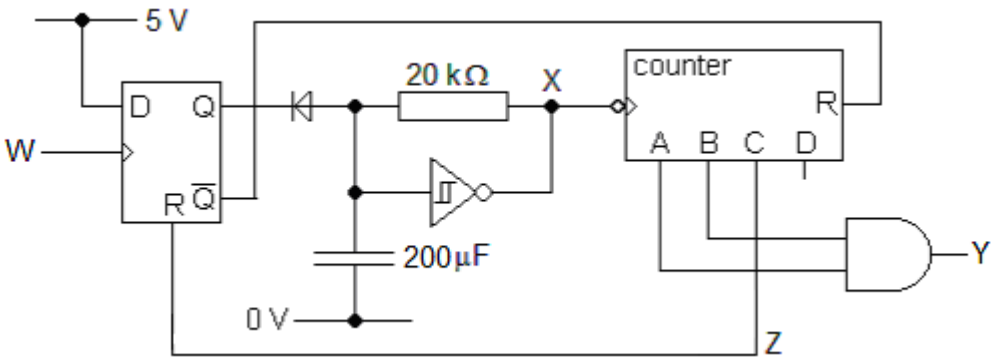
1 Complete the block diagram for a one-shot sequencer. Choose from these words.

counter D flip-flop oscillator output reset logic sequence logic



2 Complete the sentences for the one-shot sequencer shown below. Choose from these words.

falling four high low one pulses resets rising sets three two

Before the system is triggered, Y is _____.
The system is triggered by a _____ edge at W.
This _____ the flip-flop, allowing _____ to appear at X.
Each _____ edge at X makes the counter output increase by _____.
After _____ of these pulses, Z goes _____ and the flip-flop is _____ .
The sequence at Y has _____ steps, each of which lasts about _____ seconds.
This requires the use of a _____-bit counter.